FIG.1

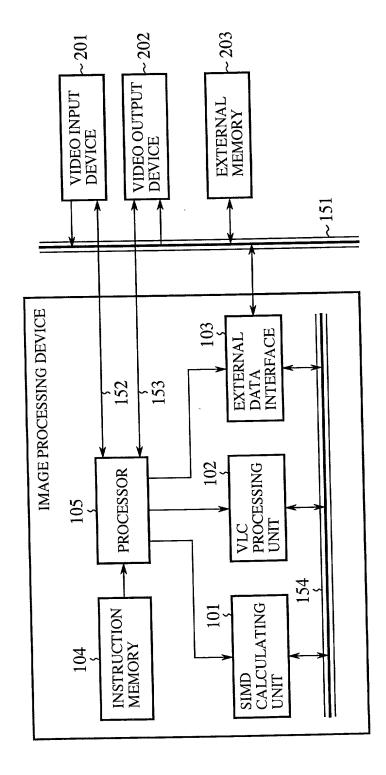
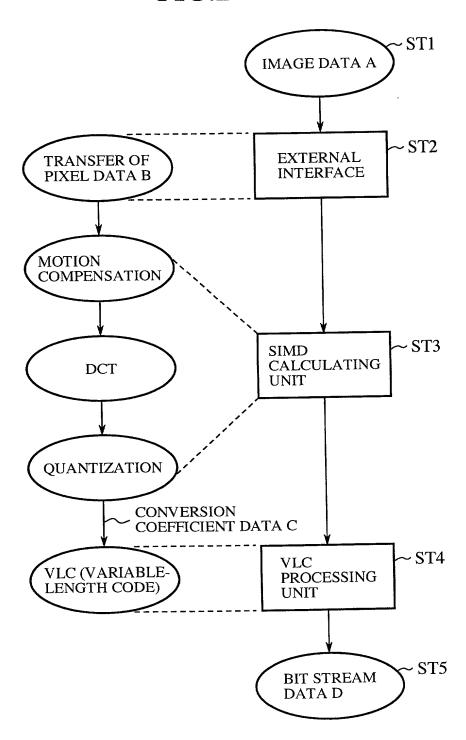


FIG.2



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FIG.4

| | | ` | | | ` |
|-------------------------|----------|--------|------------------------|----------|-----|
| X1 X2 X3 X4 X5 X6 X7 X8 | | | Y1Y9Y17Y25Y33Y41Y49Y57 | | |
| X9 | _~ | X16 | Y2 | | ! |
| X17 | | 1 | Y3 | | 1 |
| X25 | | i | Y4 | | |
| X33 | | ! ! | Y5 | | |
| X41 | | ! | Y6 | | |
| X49 | | | Y7 | | 1 |
| X57 | | X64 | Y8Y16 | | Y64 |
| | | J | | |) |
| | MATRIX X | | | MATRIX Y | |

FIG.5

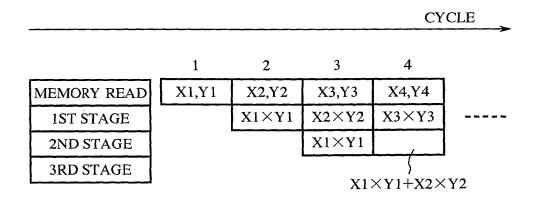
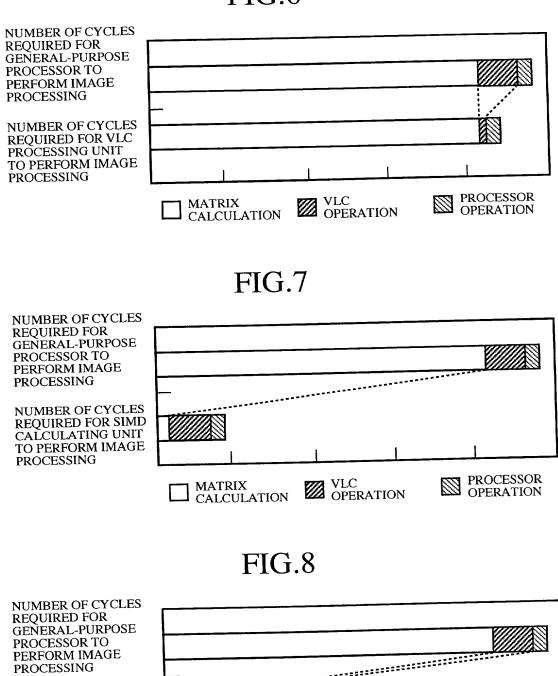


FIG.6



NUMBER OF CYCLES REQUIRED FOR IMAGE PROCESSING DEVICE ACCORDING TO THE PRESENT INVENTION TO PERFORM IMAGE

PROCESSING

